



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Group Art Unit: 2785
ELIYAHOU HARARI, ROBERT D.)
NORMAN and SANJAY MEHROTRA) Examiner: P. Chung
Serial No.: 08/931,133)
Filed: September 18, 1997)
For: MULTI-STATE FLASH EEPROM)
SYSTEM WITH CACHE MEMORY)
San Francisco, California

#9/C
RECEIVED
98 OCT 16 AM 7:49
AMERICAN
RECEIVED
OCT 16 1998

Assistant Commissioner of Patents

Washington, D.C. 20231

11/13/1998 DFCRTE 00000085 08931133

01 FC:102 82.00 OP
02 FC:103 412.00 CH 620.00 OP

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on October 6, 1998.

Brenda J. Dolly

Signature

Date

Adjustment date: 11/13/1998 DFCRTE
10/15/1998 AIBRAHM 00000085 08931133
02 FC:102 -902.00 OP

10/15/1998 AIBRAHM 00000085 08931133

AMENDMENT

02 FC:102 Sir: 902.00 OP

Responsive to the Office Action dated May 20, 1998, please amend the above-identified patent application as follows:

IN THE CLAIMS:

Amend the following existing claims, as follows:

1. (Amended) A method for extending the life of Flash EEPROM memory in a Flash EEPROM system, comprising the steps of:
temporarily storing data files from a host system intended for the Flash EEPROM memory in a cache memory, said cache memory able to undergo significantly more write/erase cycles than the Flash EEPROM memory;